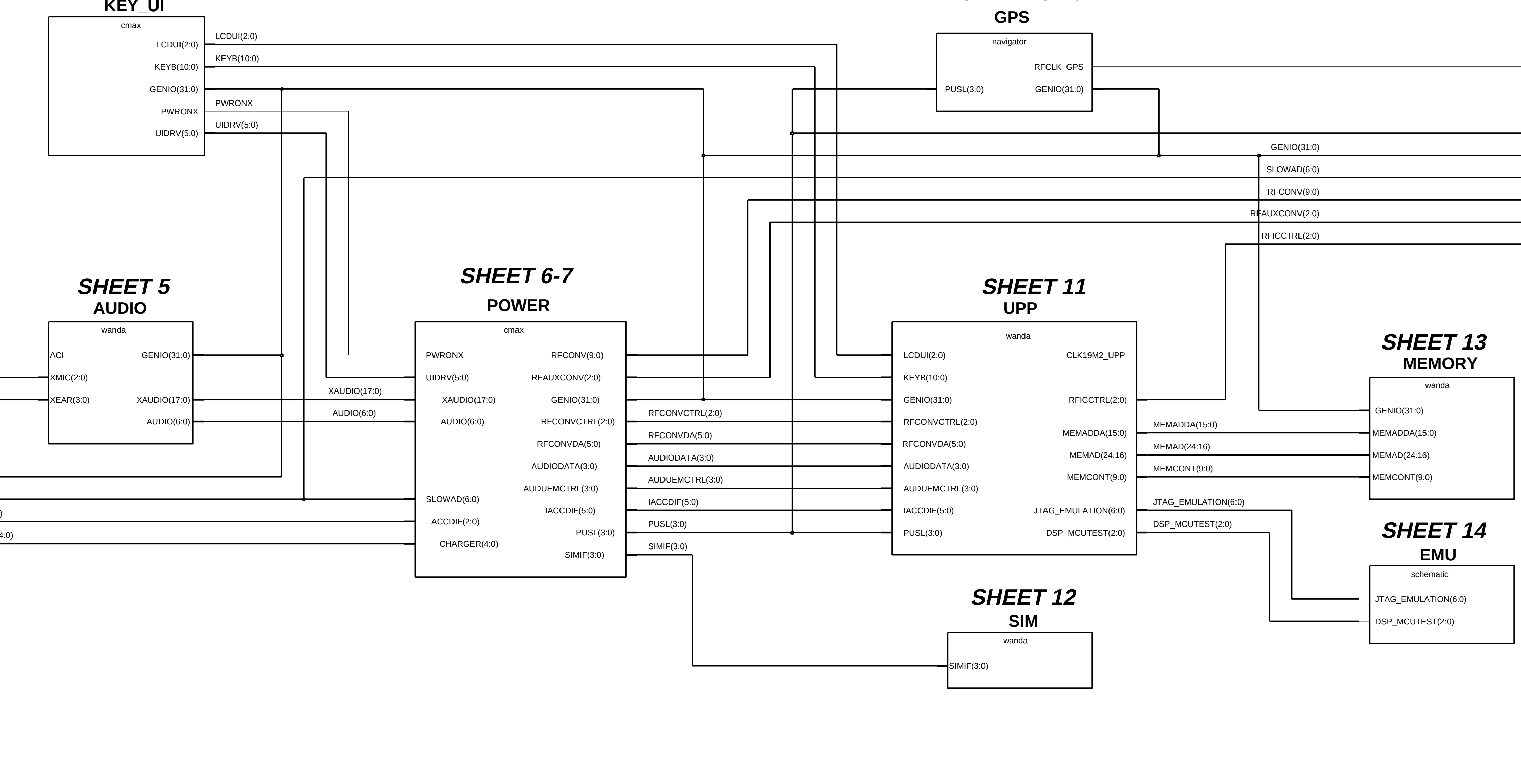
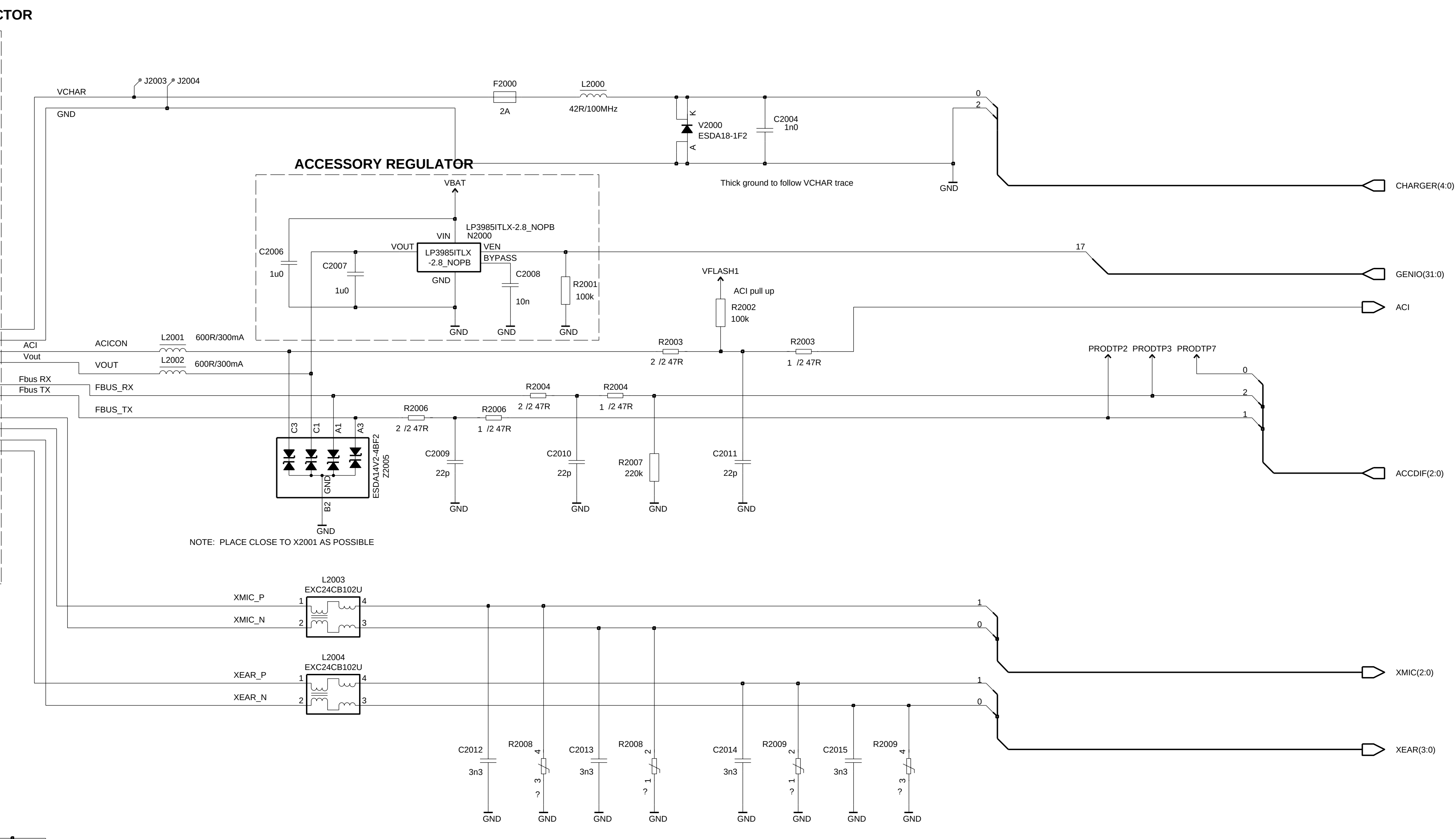
Left port	Net	Block pin (left)	Block pin (right)	Net	Right port
GENIO(31:0)	GENIO(31:0)	GENIO(31:0)	TX_GATE	TX_GATE	TX_GATE
			SYNTH_LE	SYNTH_LE	SYNTH_LE
			RX_IF_AGC	RX_IF_AGC	RX_IF_AGC
			TX_AGC2	TX_AGC2	TX_AGC2
			TX_AGC1	TX_AGC1	TX_AGC1
RFCONV(9:0)		RFCONV(9:0)	RX_IP	RX_IP	RX_IP
			RX_IN	RX_IN	RX_IN
			RX_QP	RX_QP	RX_QP
			RX_QN	RX_QN	RX_QN
			TX_IP	TX_IP	TX_IP
			TX_IN	TX_IN	TX_IN
			TX_QP	TX_QP	TX_QP
			TX_QN	TX_QN	TX_QN
			VREFRF	VREFRF	VREFRF01
RFAUXCONV(2:0)		RFAUXCONV(2:0)	AUXDAC	AUXDAC	PA_BIAS
			PWROUT	PWROUT	PWROUT
			AFC	AFC	AFC
RFICCTRL(2:0)		RFICCTRL(2:0)	RF_BUS_CLK	RF_BUS_CLK	RF_BUS_CLK
			RF_BUS_DATA	RF_BUS_DATA	RF_BUS_DATA
			RF_BUS_EN	RF_BUS_EN	RF_BUS_EN
SLOWAD(6:0)		SLOWAD(6:0)	FALSE_DET	FALSE_DET	FALSE_DET
			PA_TEMP	PA_TEMP	PA_TEMP
PUSL(3:0)	PUSL(3:0)	PUSL(3:0)	PURX	PURX	PURX
CLK19M2_UPP					CLK19M2_UPP
CLK19M2_GPS					CLK19M2_GPS



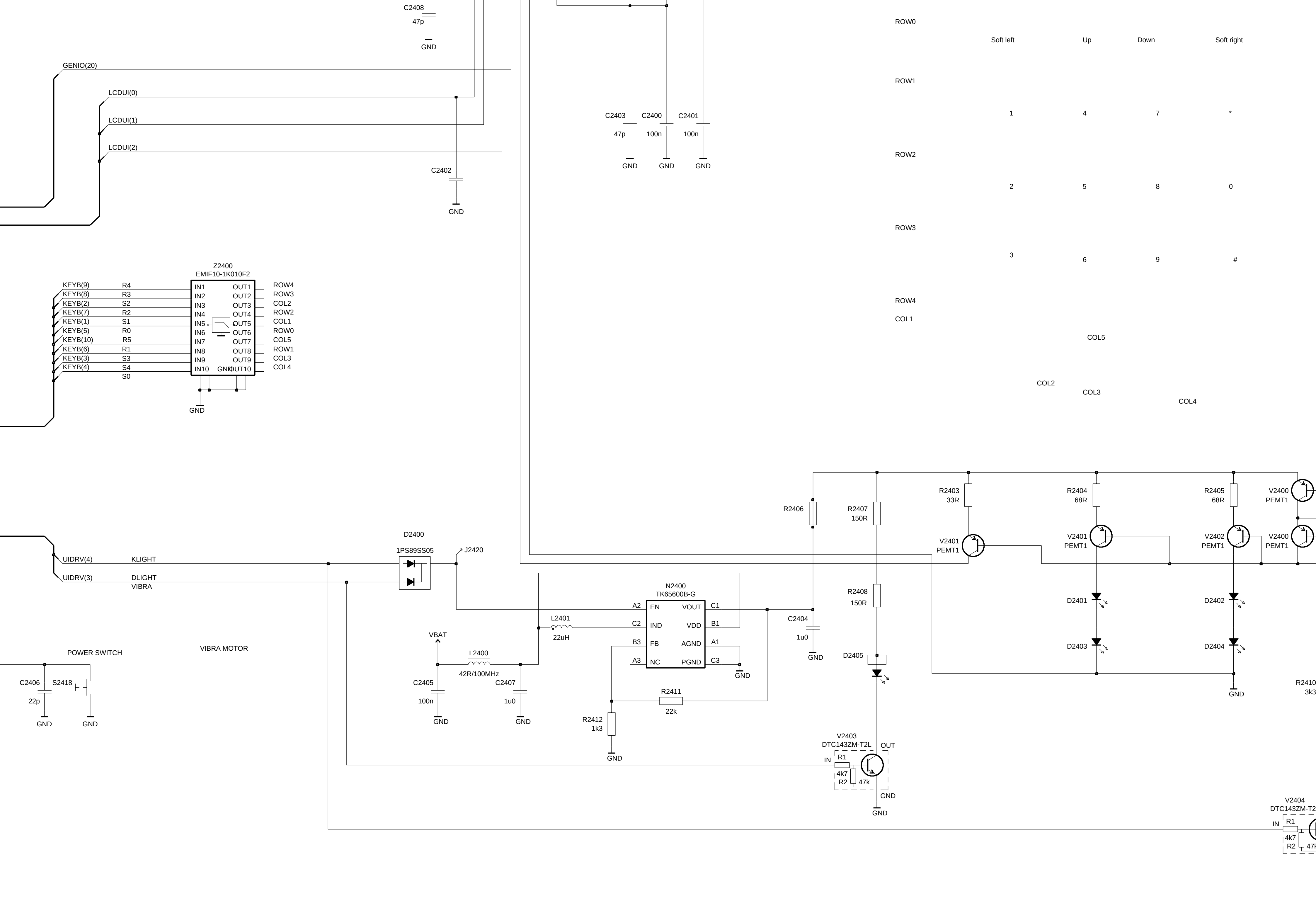
KEY_UI
cmax
LCDUI(2:0)
KEYB(10:0)
GENIO(31:0)
PWRONX
UIDRV(5:0)
GPS
navigator
RFCLK_GPS
PUSL(3:0)
GENIO(31:0)
SLOWAD(6:0)
RFCONV(9:0)
RFAUXCONV(2:0)
RFICCTRL(2:0)
SHEET 5
AUDIO
wanda
ACI
XMIC(2:0)
XEAR(3:0)
XAUDIO(17:0)
AUDIO(6:0)
SHEET 6-7
POWER
cmax
PWRONX
UIDRV(5:0)
SLOWAD(6:0)
ACCDIF(2:0)
CHARGER(4:0)
RFCONVCTRL(2:0)
RFCONVDA(5:0)
AUDIODATA(3:0)
AUDUEMCTRL(3:0)
IACCDIF(5:0)
SIMIF(3:0)
SHEET 11
UPP
wanda
CLK19M2_UPP
MEMADDA(15:0)
MEMAD(24:16)
MEMCONT(9:0)
JTAG_EMULATION(6:0)
DSP_MCUTEST(2:0)
SHEET 13
MEMORY
SHEET 14
EMU
schematic
SHEET 12
SIM

J2002 J2008
J2001
J2000 J2007
BSI
C2000 10n GND
C2001 47p GND
C2002 10U GND
C2003 47p GND
J2005
BTEMP
R2000 47k
GND
0
1
SLOWAD(6:0)

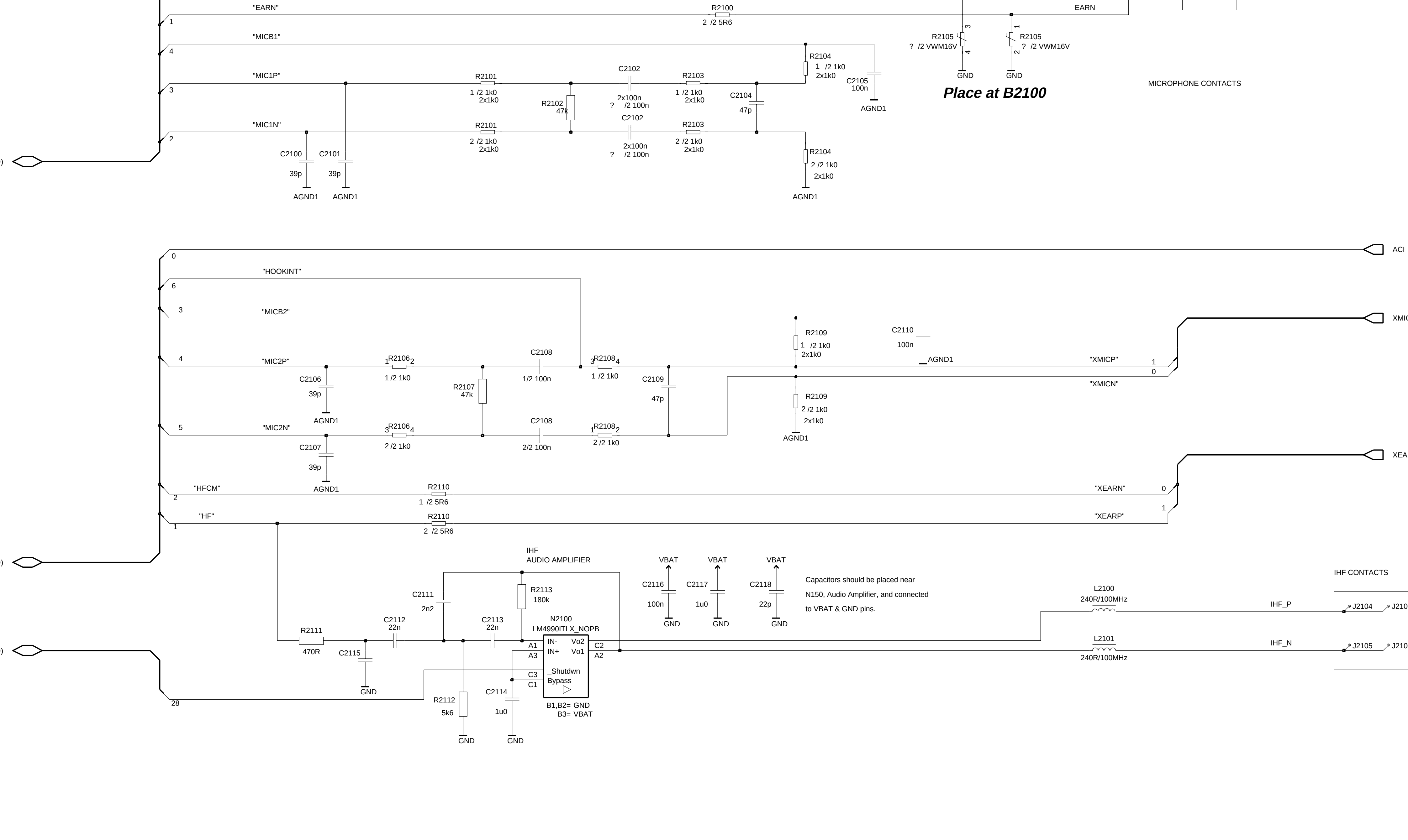
CTOR

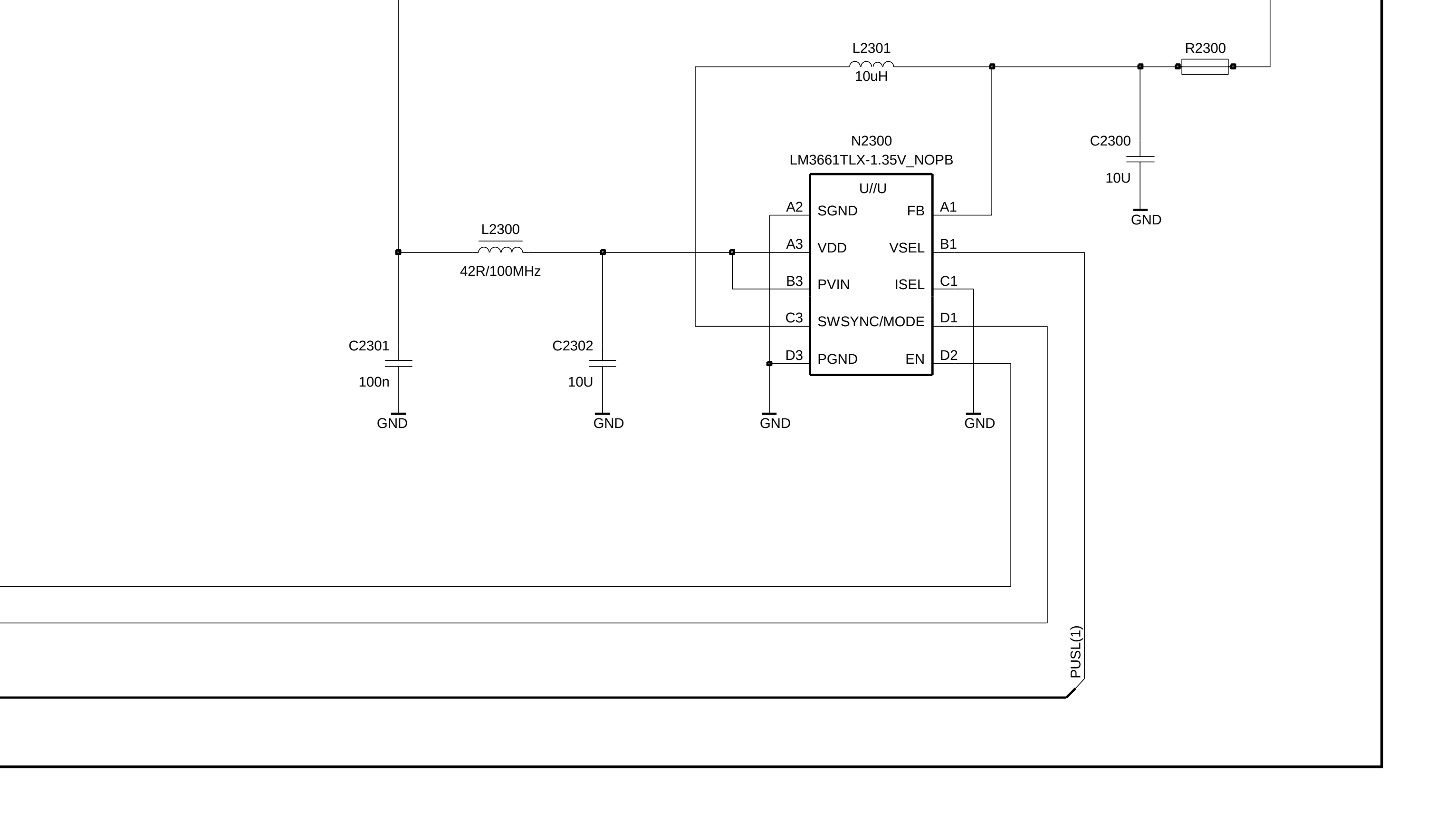


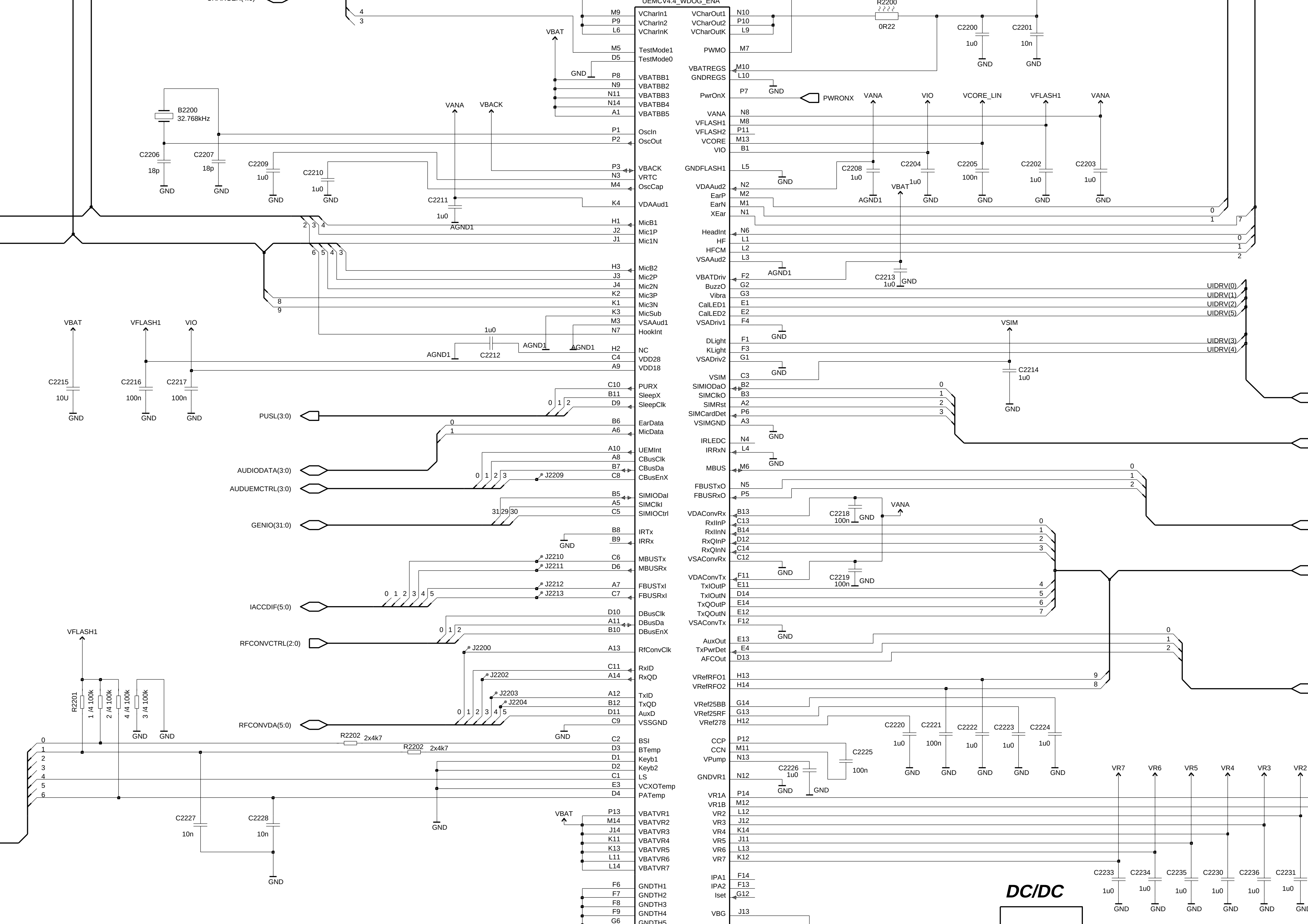
E2000 E2001 E2002 E2003 E2004 E2205 E2006 E2007

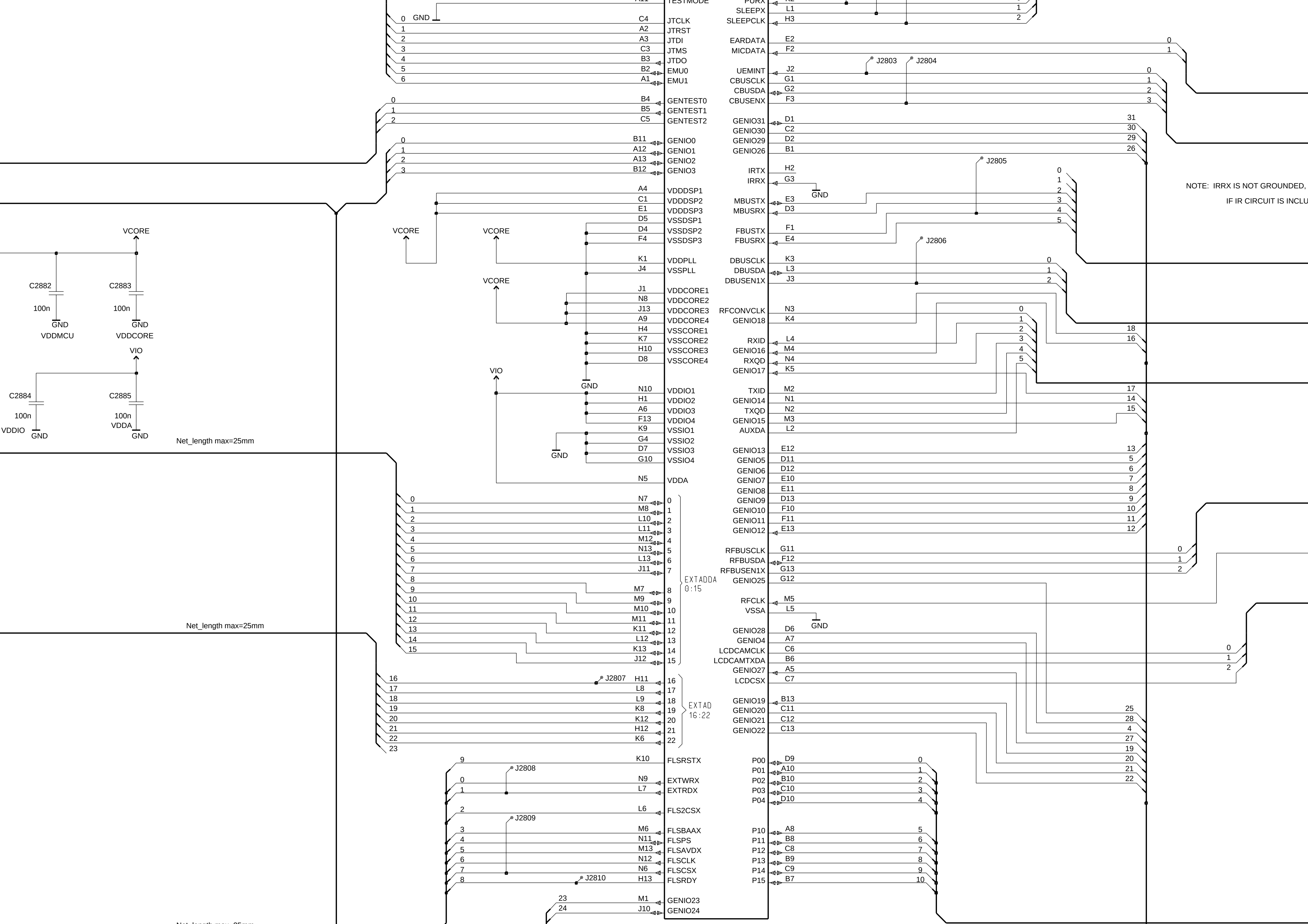


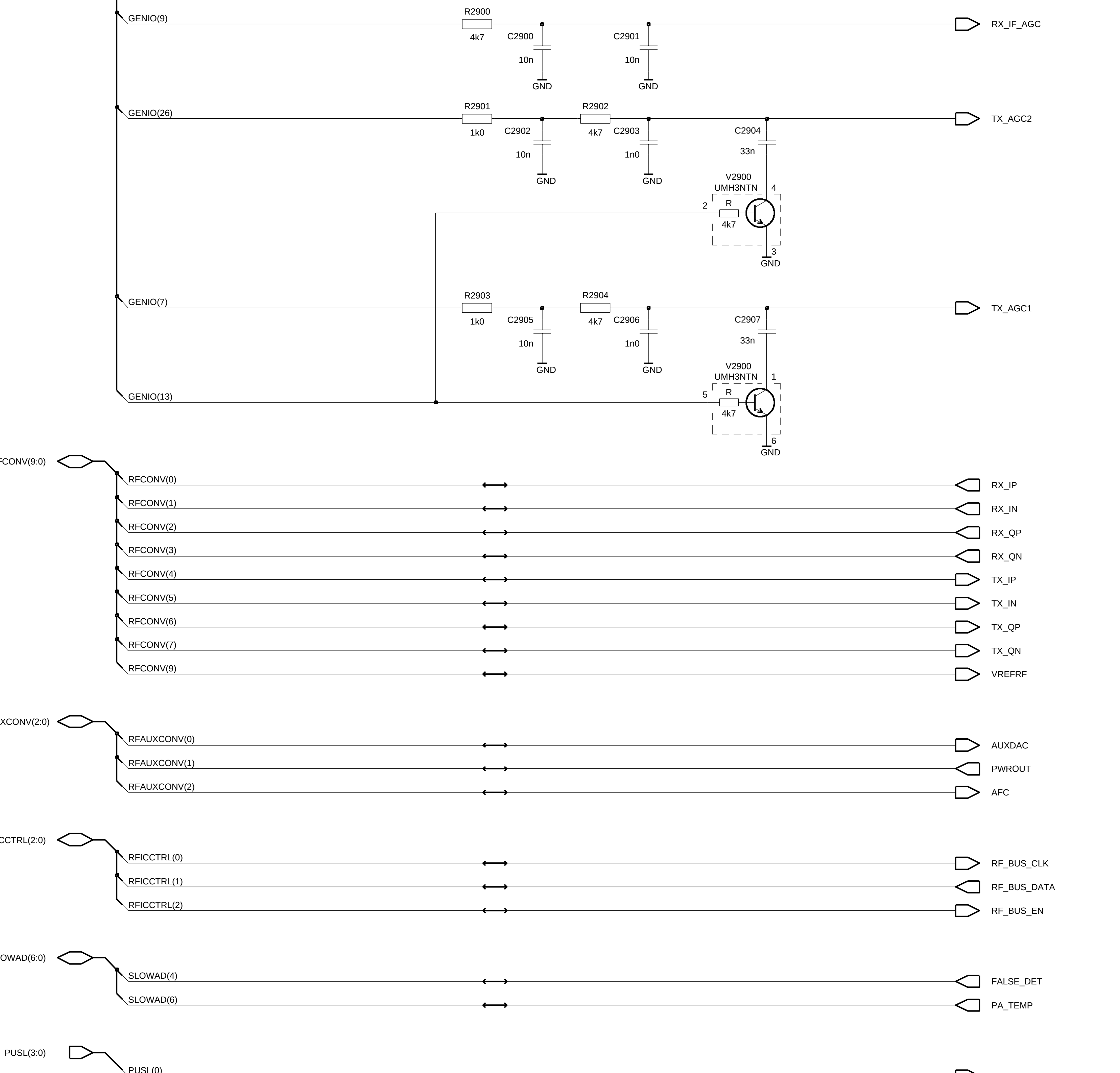
C2408
47p
GND
GENIO(20)
LCDUI(0)
LCDUI(1)
LCDUI(2)
C2402
GND
C2403
47p
C2400
100n
C2401
100n
GND
GND
GND
ROW0
Soft left
Up
Down
Soft right
ROW1
1
4
7
*
ROW2
2
5
8
0
ROW3
3
6
9
#
ROW4
COL1
COL5
COL2
COL3
COL4
Z2400
EMIF10-1K010F2
KEYB(9) R4 IN1 OUT1 ROW4
KEYB(8) R3 IN2 OUT2 ROW3
KEYB(2) S2 IN3 OUT3 COL2
KEYB(7) R2 IN4 OUT4 ROW2
KEYB(1) S1 IN5 OUT5 COL1
KEYB(5) R0 IN6 OUT6 ROW0
KEYB(10) R5 IN7 OUT7 COL5
KEYB(6) R1 IN8 OUT8 ROW1
KEYB(3) S3 IN9 OUT9 COL3
KEYB(4) S4 IN10 GND OUT10 COL4
S0
GND
UIDRV(4)
KLIGHT
UIDRV(3)
DLIGHT
VIBRA
D2400
1PS89SS05
J2420
N2400
TK65600B-G
A2 EN VOUT C1
C2 IND VDD B1
B3 FB AGND A1
A3 NC PGND C3
GND
L2401
22uH
VBAT
L2400
42R/100MHz
C2405
100n
C2407
1u0
GND
GND
R2411
22k
R2412
1k3
GND
C2404
1u0
GND
R2406
R2407
150R
R2408
150R
D2405
V2403
DTC143ZM-T2L
OUT
IN
R1
4k7
R2
47k
GND
GND
R2403
33R
R2404
68R
R2405
68R
V2400
PEMT1
V2401
PEMT1
V2401
PEMT1
V2402
PEMT1
V2400
PEMT1
D2401
D2403
D2402
D2404
GND
R2410
3k3
V2404
DTC143ZM-T2
IN
R1
4k7
R2
47k
POWER SWITCH
VIBRA MOTOR
C2406
22p
S2418
GND
GND


"EARN"
R2100
2 /2 5R6
EARN
R2105
? /2 VWM16V
R2105
? /2 VWM16V
GND
GND
"MICB1"
R2104
1 /2 1k0
2x1k0
C2105
100n
AGND1
"MIC1P"
R2101
1 /2 1k0
2x1k0
C2102
2x100n
? /2 100n
R2103
1 /2 1k0
2x1k0
C2104
47p
R2102
47k
Place at B2100
MICROPHONE CONTACTS
"MIC1N"
R2101
2 /2 1k0
2x1k0
C2102
2x100n
? /2 100n
R2103
2 /2 1k0
2x1k0
R2104
2 /2 1k0
2x1k0
C2100
39p
C2101
39p
AGND1
AGND1
AGND1
ACI
"HOOKINT"
"MICB2"
R2109
1 /2 1k0
2x1k0
C2110
100n
AGND1
XMIC
"MIC2P"
R2106
1 /2 1k0
C2108
1/2 100n
R2108
1 /2 1k0
C2109
47p
"XMICP"
"XMICN"
C2106
39p
R2107
47k
R2109
2 /2 1k0
2x1k0
AGND1
AGND1
"MIC2N"
R2106
2 /2 1k0
C2108
2/2 100n
R2108
2 /2 1k0
C2107
39p
AGND1
XEA
"HFCM"
R2110
1 /2 5R6
"XEARN"
"HF"
R2110
2 /2 5R6
"XEARP"
IHF
AUDIO AMPLIFIER
VBAT
VBAT
VBAT
C2116
100n
C2117
1u0
C2118
22p
GND
GND
GND
Capacitors should be placed near
N150, Audio Amplifier, and connected
to VBAT & GND pins.
IHF CONTACTS
L2100
240R/100MHz
IHF_P
J2104
L2101
240R/100MHz
IHF_N
J2105
C2111
2n2
R2113
180k
C2112
22n
C2113
22n
N2100
LM4990ITLX_NOPB
R2111
470R
C2115
GND
IN-
IN+
Vo2
Vo1
_Shutdwn
Bypass
A1
A3
C3
C1
C2
A2
R2112
5k6
C2114
1u0
B1,B2= GND
B3= VBAT
GND
GND
28


L2301
10uH
R2300
N2300
LM3661TLX-1.35V_NOPB
U//U
C2300
10U
GND
A2 SGND
FB A1
A3 VDD
VSEL B1
B3 PVIN
ISEL C1
C3 SWSYNC/MODE
D1
D3 PGND
EN D2
L2300
42R/100MHz
C2301
100n
C2302
10U
GND
GND
GND
GND
PUSL(1)


UEMCV4.4_WDOG_ENA
VCharIn1
VCharIn2
VCharInK
TestMode1
TestMode0
VBATBB1
VBATBB2
VBATBB3
VBATBB4
VBATBB5
OscIn
OscOut
VBACK
VRTC
OscCap
VDAAud1
MicB1
Mic1P
Mic1N
MicB2
Mic2P
Mic2N
Mic3P
Mic3N
MicSub
VSAAud1
HookInt
NC
VDD28
VDD18
PURX
SleepX
SleepClk
EarData
MicData
UEMInt
CBusClk
CBusDa
CBusEnX
SIMIODaI
SIMClkI
SIMIOCtrl
IRTx
IRRx
MBUSTx
MBUSRx
FBUSTxI
FBUSRxI
DBusClk
DBusDa
DBusEnX
RfConvClk
RxID
RxQD
TxID
TxQD
AuxD
VSSGND
BSI
BTemp
Keyb1
Keyb2
LS
VCXOTemp
PATemp
VBATVR1
VBATVR2
VBATVR3
VBATVR4
VBATVR5
VBATVR6
VBATVR7
GNDTH1
GNDTH2
GNDTH3
GNDTH4
GNDTH5
VCharOut1
VCharOut2
VCharOutK
PWMO
VBATREGS
GNDREGS
PwrOnX
VANA
VFLASH1
VFLASH2
VCORE
VIO
GNDFLASH1
VDAAud2
EarP
EarN
XEar
HeadInt
HF
HFCM
VSAAud2
VBATDriv
BuzzO
Vibra
CalLED1
CalLED2
VSADriv1
DLight
KLight
VSADriv2
VSIM
SIMIODaO
SIMClkO
SIMRst
SIMCardDet
VSIMGND
IRLEDC
IRRxN
MBUS
FBUSTxO
FBUSRxO
VDAConvRx
RxIInP
RxIInN
RxQInP
RxQInN
VSAConvRx
VDAConvTx
TxIOutP
TxIOutN
TxQOutP
TxQOutN
VSAConvTx
AuxOut
TxPwrDet
AFCOut
VRefRFO1
VRefRFO2
VRef25BB
VRef25RF
VRef278
CCP
CCN
VPump
GNDVR1
VR1A
VR1B
VR2
VR3
VR4
VR5
VR6
VR7
IPA1
IPA2
Iset
VBG
R2200 0R22
C2200 1u0
C2201 10n
PWRONX
B2200 32.768kHz
C2206 18p
C2207 18p
C2209 1u0
C2210 1u0
C2211 1u0
C2212 1u0
C2208 1u0
C2204 1u0
C2205 100n
C2202 1u0
C2203 1u0
C2213 1u0
C2214 1u0
C2215 10U
C2216 100n
C2217 100n
C2218 100n
C2219 100n
C2220 1u0
C2221 100n
C2222 1u0
C2223 1u0
C2224 1u0
C2225 100n
C2226 1u0
C2227 10n
C2228 10n
C2233 1u0
C2234 1u0
C2235 1u0
C2230 1u0
C2236 1u0
C2231 1u0
R2201
1 /4 100k
2 /4 100k
4 /4 100k
3 /4 100k
R2202 2x4k7
R2202 2x4k7
J2200
J2202
J2203
J2204
J2209
J2210
J2211
J2212
J2213
PUSL(3:0)
AUDIODATA(3:0)
AUDUEMCTRL(3:0)
GENIO(31:0)
IACCDIF(5:0)
RFCONVCTRL(2:0)
RFCONVDA(5:0)
UIDRV(0)
UIDRV(1)
UIDRV(2)
UIDRV(5)
UIDRV(3)
UIDRV(4)
VBAT
VANA
VBACK
VIO
VCORE_LIN
VFLASH1
VSIM
AGND1
GND
VR7
VR6
VR5
VR4
VR3
VR2
DC/DC


NOTE: IRRX IS NOT GROUNDED,
IF IR CIRCUIT IS INCLU
Net_length max=25mm
Net_length max=25mm
VCORE
VIO
VDDA
C2882
C2883
C2884
C2885
100n
VDDMCU
VDDCORE
VDDIO
GND
J2803
J2804
J2805
J2806
J2807
J2808
J2809
J2810
EXTADDA 0:15
EXTAD 16:22
TESTMODE
JTCLK
JTRST
JTDI
JTMS
JTDO
EMU0
EMU1
GENTEST0
GENTEST1
GENTEST2
GENIO0
GENIO1
GENIO2
GENIO3
VDDDSP1
VDDDSP2
VDDDSP3
VSSDSP1
VSSDSP2
VSSDSP3
VDDPLL
VSSPLL
VDDCORE1
VDDCORE2
VDDCORE3
VDDCORE4
VSSCORE1
VSSCORE2
VSSCORE3
VSSCORE4
VDDIO1
VDDIO2
VDDIO3
VDDIO4
VSSIO1
VSSIO2
VSSIO3
VSSIO4
FLSRSTX
EXTWRX
EXTRDX
FLS2CSX
FLSBAAX
FLSPS
FLSAVDX
FLSCLK
FLSCSX
FLSRDY
GENIO23
GENIO24
PURX
SLEEPX
SLEEPCLK
EARDATA
MICDATA
UEMINT
CBUSCLK
CBUSDA
CBUSENX
GENIO31
GENIO30
GENIO29
GENIO26
IRTX
IRRX
MBUSTX
MBUSRX
FBUSTX
FBUSRX
DBUSCLK
DBUSDA
DBUSEN1X
RFCONVCLK
GENIO18
RXID
GENIO16
RXQD
GENIO17
TXID
GENIO14
TXQD
GENIO15
AUXDA
GENIO13
GENIO5
GENIO6
GENIO7
GENIO8
GENIO9
GENIO10
GENIO11
GENIO12
RFBUSCLK
RFBUSDA
RFBUSEN1X
GENIO25
RFCLK
VSSA
GENIO28
GENIO4
LCDCAMCLK
LCDCAMTXDA
GENIO27
LCDCSX
GENIO19
GENIO20
GENIO21
GENIO22
P00
P01
P02
P03
P04
P10
P11
P12
P13
P14
P15


GENIO(9)
R2900
4k7
C2900
10n
GND
C2901
10n
GND
RX_IF_AGC
GENIO(26)
R2901
1k0
C2902
10n
GND
R2902
4k7
C2903
1n0
GND
C2904
33n
TX_AGC2
V2900
UMH3NTN
R
4k7
2
4
3
GND
GENIO(7)
R2903
1k0
C2905
10n
GND
R2904
4k7
C2906
1n0
GND
C2907
33n
TX_AGC1
V2900
UMH3NTN
R
4k7
5
1
6
GND
GENIO(13)
FCONV(9:0)
RFCONV(0)
RX_IP
RFCONV(1)
RX_IN
RFCONV(2)
RX_QP
RFCONV(3)
RX_QN
RFCONV(4)
TX_IP
RFCONV(5)
TX_IN
RFCONV(6)
TX_QP
RFCONV(7)
TX_QN
RFCONV(9)
VREFRF
XCONV(2:0)
RFAUXCONV(0)
AUXDAC
RFAUXCONV(1)
PWROUT
RFAUXCONV(2)
AFC
CCTRL(2:0)
RFICCTRL(0)
RF_BUS_CLK
RFICCTRL(1)
RF_BUS_DATA
RFICCTRL(2)
RF_BUS_EN
OWAD(6:0)
SLOWAD(4)
FALSE_DET
SLOWAD(6)
PA_TEMP
PUSL(3:0)
PUSL(0)

D3000
K5N64168TA-DE90

COMBO 64M NOR
16M UTRAM

Bus	Pin	Signal
16	B2	16 (A)
17	A9	
18	B8	
19	A8	
20	B3	20 (A)
21	A2	A21/_LB
22	B7	_WP/_CS
23	A10	_UB
5	B4	_AVD
0	A6	_WE
1	C10	_OE
4		
6	A4	CLK
9	B6	_RESET
7	B9	_CEF
8	A1	RDY
3		

NOR/
RAM

NOR

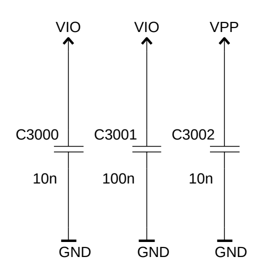
NC B5
NC 1
NC 2
NC 3
NC 4

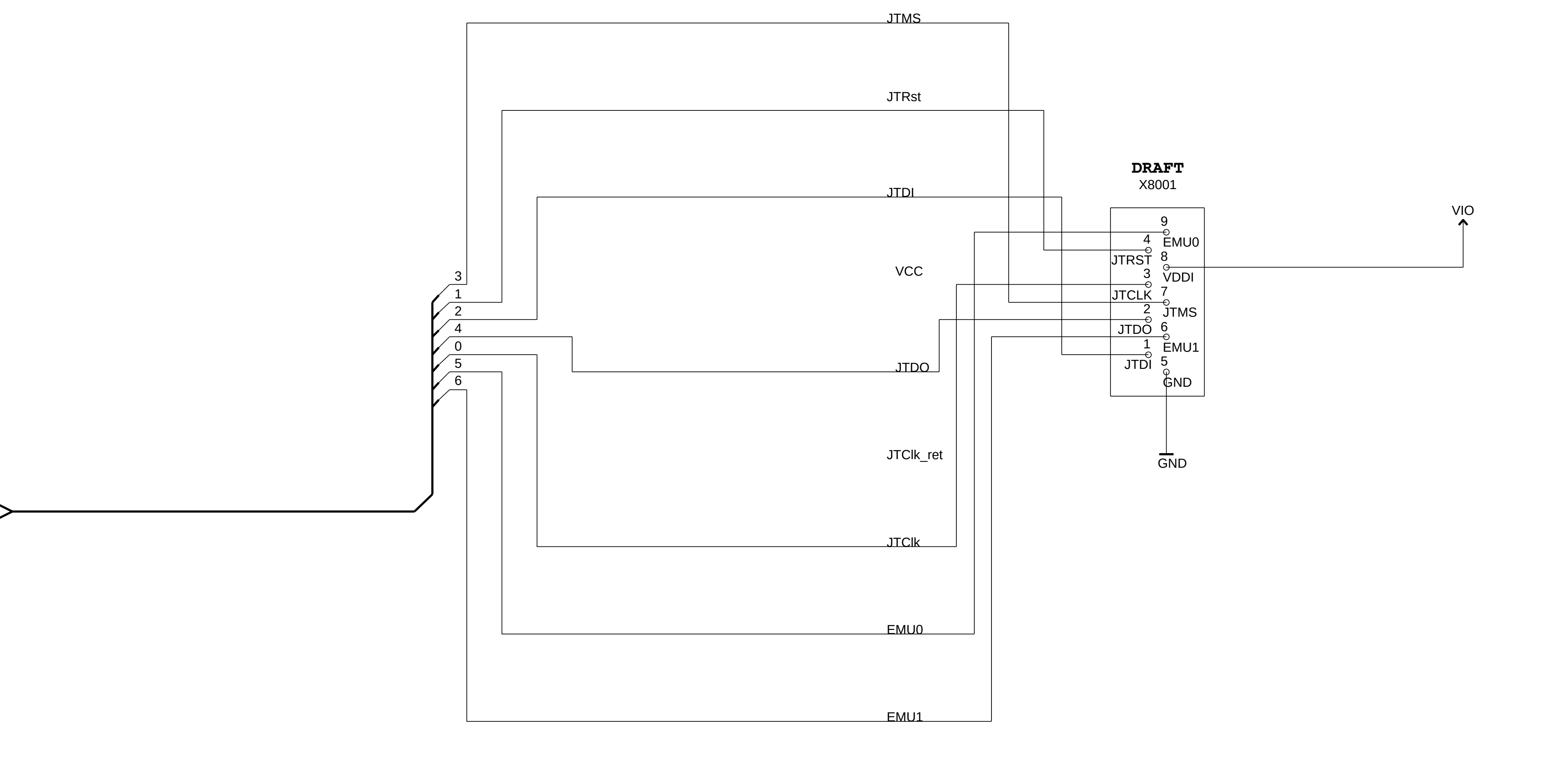
R3000 4k7 VPP

D10 ADDRESS/DATA I/O

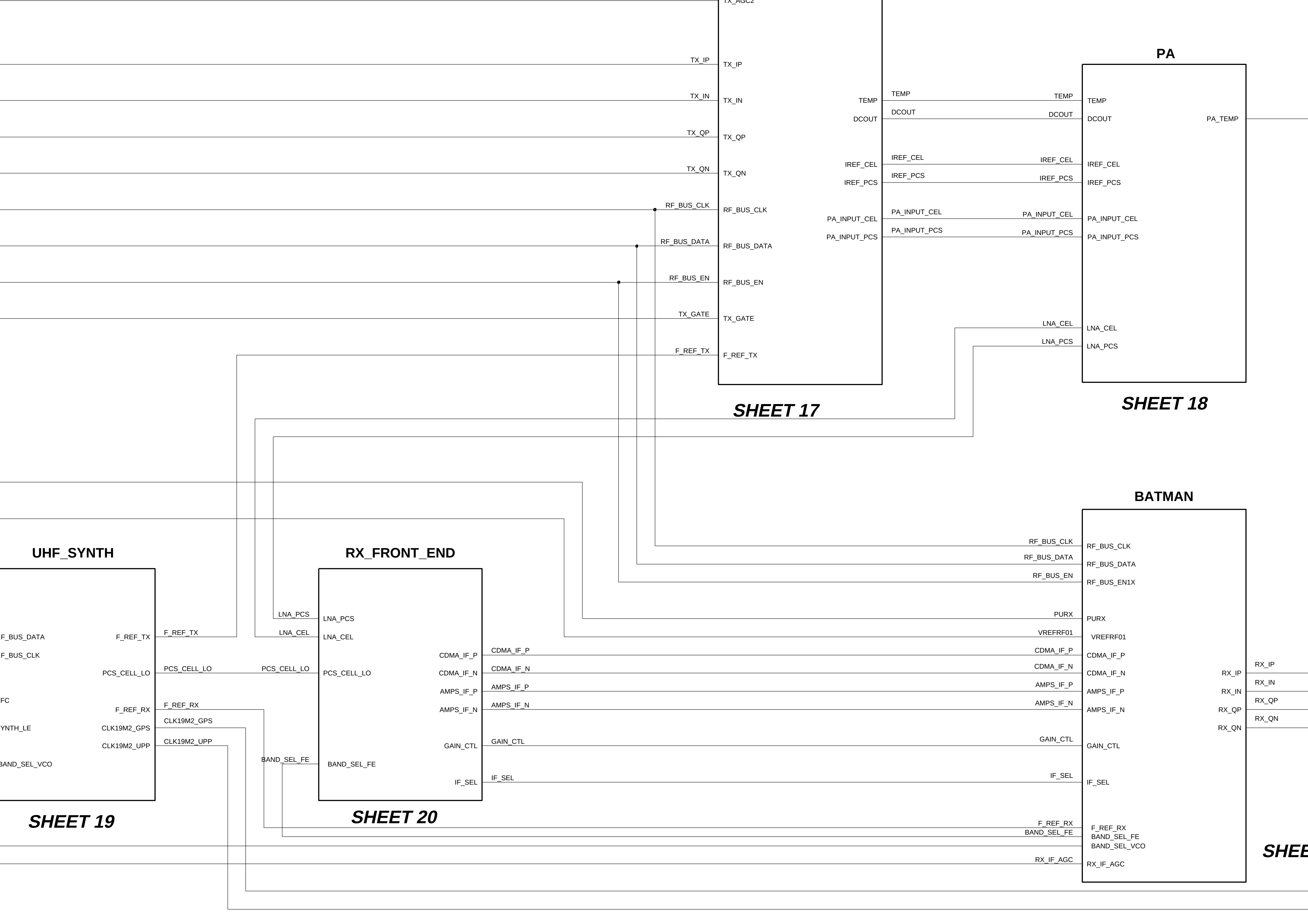
Bus	Pin
0	D10
1	D9
2	C7
3	C6
4	D5
5	D4
6	C3
7	C2
8	C9
9	C8
10	D7
11	D6
12	C5
13	C4
14	D2
15	D1

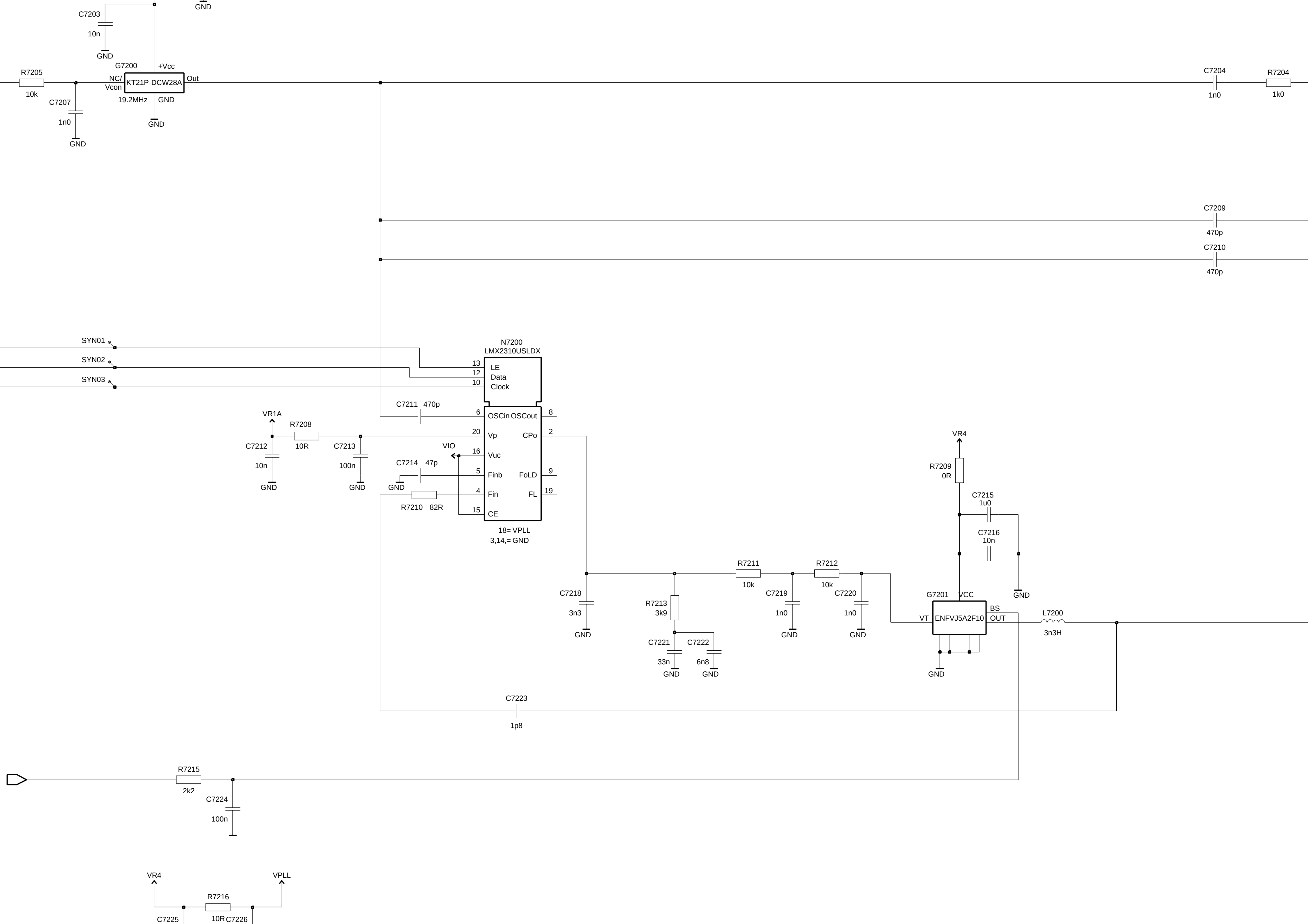
A5,B1,D8= VIO
A7= VPP
A3,B10,C1,D3= GND



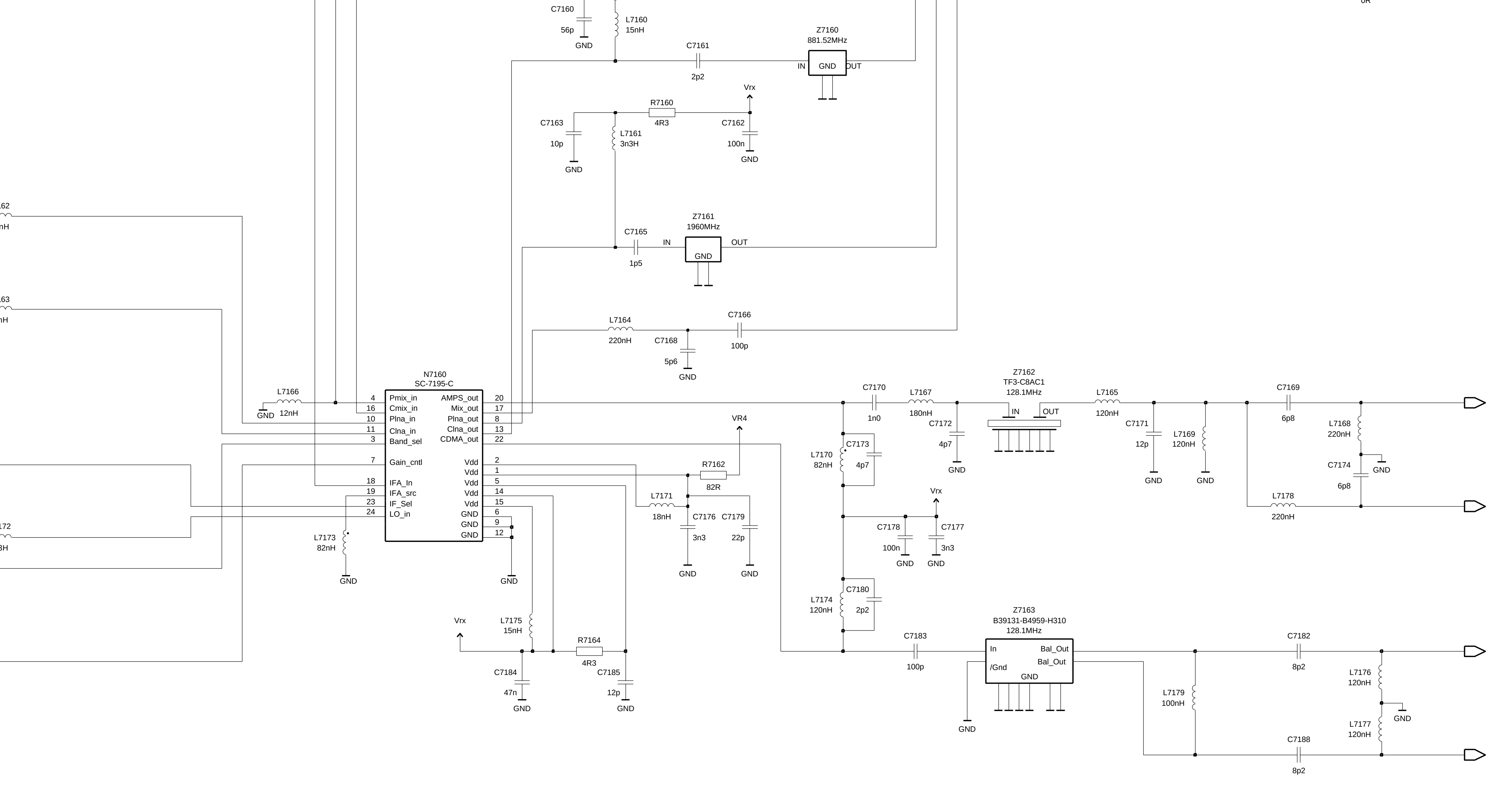


JTMS
JTRst
DRAFT
X8001
JTDI
VIO
9
4
EMU0
JTRST
8
VDDI
3
JTCLK
7
JTMS
2
JTDO
6
EMU1
1
JTDI
5
GND
VCC
3
1
2
4
0
5
6
JTDO
GND
JTClk_ret
JTClk
EMU0
EMU1

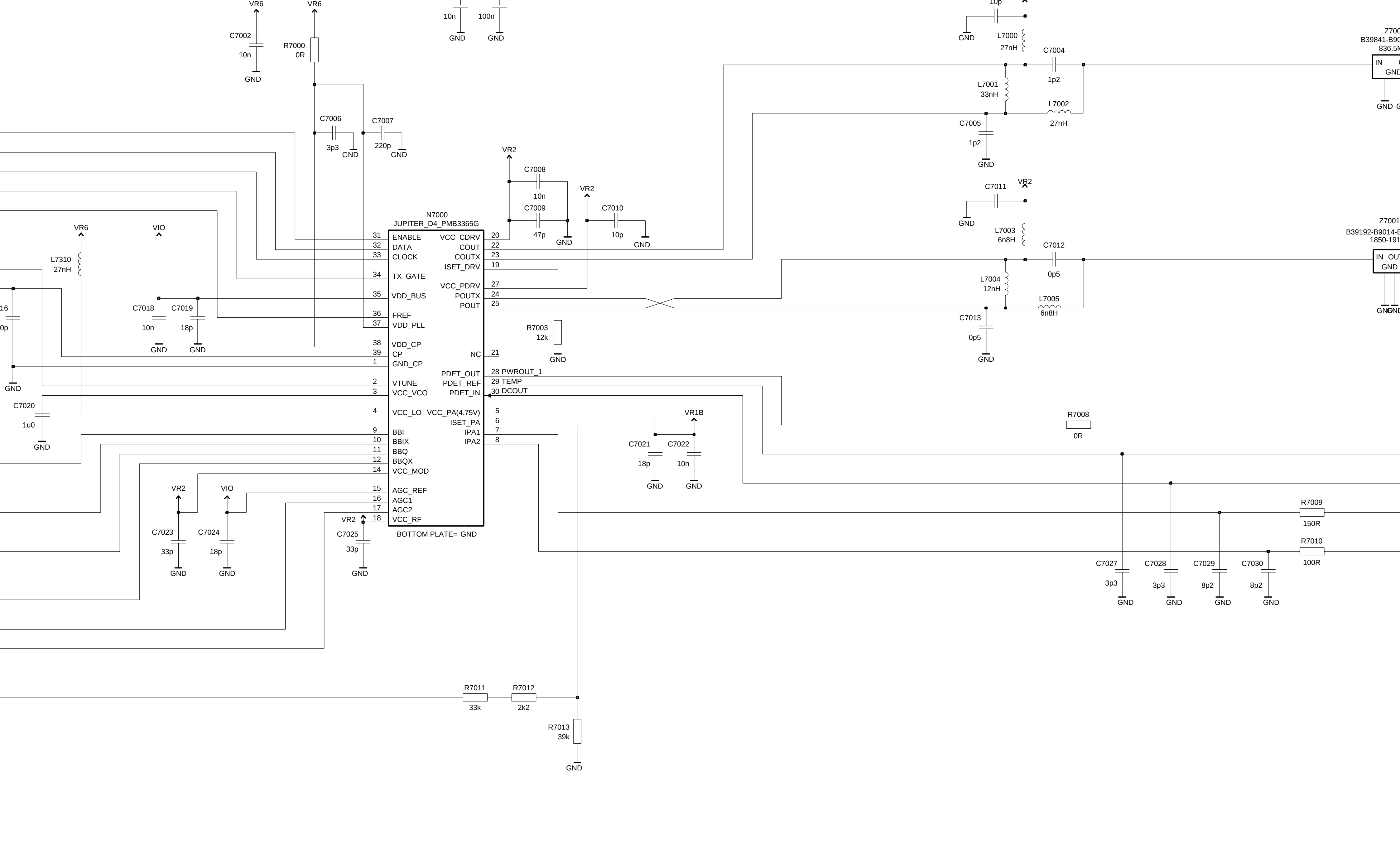

TX_AGC2
TX_IP
TX_IN
TX_QP
TX_QN
RF_BUS_CLK
RF_BUS_DATA
RF_BUS_EN
TX_GATE
F_REF_TX
TEMP
DCOUT
IREF_CEL
IREF_PCS
PA_INPUT_CEL
PA_INPUT_PCS
SHEET 17
PA
PA_TEMP
LNA_CEL
LNA_PCS
SHEET 18
BATMAN
RF_BUS_EN1X
PURX
VREFRF01
CDMA_IF_P
CDMA_IF_N
AMPS_IF_P
AMPS_IF_N
GAIN_CTL
IF_SEL
F_REF_RX
BAND_SEL_FE
BAND_SEL_VCO
RX_IF_AGC
RX_IP
RX_IN
RX_QP
RX_QN
UHF_SYNTH
PCS_CELL_LO
CLK19M2_GPS
CLK19M2_UPP
SHEET 19
RX_FRONT_END
LNA_PCS
LNA_CEL
SHEET 20



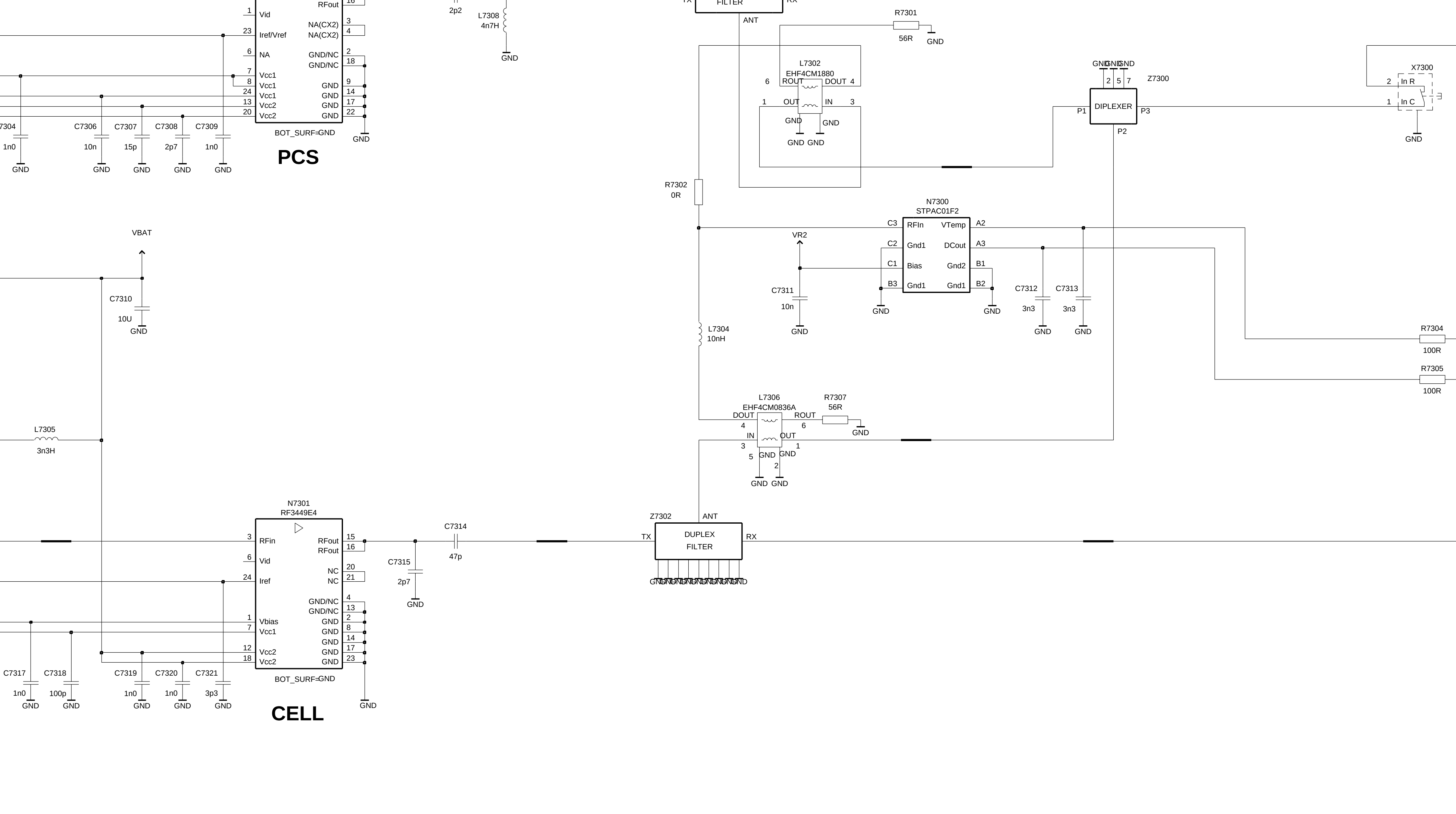
C7203
10n
GND
G7200
+Vcc
R7205
10k
NC/
Vcon
KT21P-DCW28A
Out
19.2MHz
GND
C7207
1n0
C7204
1n0
R7204
1k0
C7209
470p
C7210
470p
SYN01
SYN02
SYN03
N7200
LMX2310USLDX
LE
Data
Clock
OSCin
OSCout
Vp
CPo
Vuc
Finb
FoLD
Fin
FL
CE
18= VPLL
3,14,= GND
C7211
470p
VR1A
R7208
10R
C7212
C7213
100n
VIO
C7214
47p
R7210
82R
VR4
R7209
0R
C7215
1u0
C7216
10n
R7211
10k
R7212
10k
C7218
3n3
R7213
3k9
C7219
C7220
C7221
33n
C7222
6n8
G7201
VCC
VT
ENFVJ5A2F10
BS
OUT
L7200
3n3H
C7223
1p8
R7215
2k2
C7224
100n
VPLL
R7216
10R
C7225
C7226



LOGIC INPUTS		
BAND	GAIN_CTL	IF_SEL
0	1	0
0	0	0
1	1	0
1	0	0
0	1	1
0	0	1



N7000
JUPITER_D4_PMB3365G
ENABLE
DATA
CLOCK
TX_GATE
VDD_BUS
FREF
VDD_PLL
VDD_CP
CP
GND_CP
VTUNE
VCC_VCO
VCC_LO
BBI
BBIX
BBQ
BBQX
VCC_MOD
AGC_REF
AGC1
AGC2
VCC_RF
VCC_CDRV
COUT
COUTX
ISET_DRV
VCC_PDRV
POUTX
POUT
NC
PDET_OUT
PDET_REF
PDET_IN
VCC_PA(4.75V)
ISET_PA
IPA1
IPA2
BOTTOM PLATE= GND
PWROUT_1
TEMP
DCOUT
C7002 10n
R7000 0R
C7006 3p3
C7007 220p
C7008 10n
C7009 47p
C7010 10p
R7003 12k
L7310 27nH
C7018 10n
C7019 18p
C7020 1u0
C7021 18p
C7022 10n
C7023 33p
C7024 18p
C7025 33p
L7000 27nH
C7004 1p2
L7001 33nH
L7002 27nH
C7005 1p2
C7011
L7003 6n8H
C7012 0p5
L7004 12nH
L7005 6n8H
C7013 0p5
R7008 0R
R7009 150R
R7010 100R
C7027 3p3
C7028 3p3
C7029 8p2
C7030 8p2
R7011 33k
R7012 2k2
R7013 39k
B39841-B90
836.5M
B39192-B9014-E
1850-191
VR6
VR2
VR1B
VIO
GND



PCS
CELL
N7301
RF3449E4
N7300
STPAC01F2
DUPLEX
FILTER
DIPLEXER
Z7300
Z7302
X7300
L7302
EHF4CM1880
L7306
EHF4CM0836A
R7301
56R
R7302
0R
R7304
100R
R7305
100R
R7307
56R
L7304
10nH
L7305
3n3H
L7308
4n7H
VBAT
VR2
C7306
10n
C7307
15p
C7308
2p7
C7309
1n0
C7310
10U
C7311
10n
C7312
3n3
C7313
3n3
C7314
47p
C7315
2p7
C7317
1n0
C7318
100p
C7319
1n0
C7320
1n0
C7321
3p3
BOT_SURF=GND
GND

R7100
1k0
C7100
2n2
C7101
68n
C7102
6n8
R7101
2k2

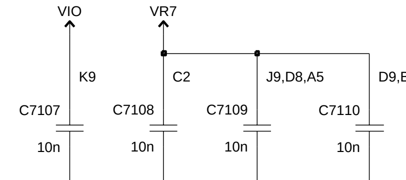
N7100
BATMAND_PBFREE

R7102
4k7
C7103
27p
C7104
1n0
V7100
BBY57-02W
C7113
6p8
L7101
27nH

VR5 — E3 — VCC_CH
K8 — CLK19M2
F1 — PD_LO2
E1 — RESO_LO2
A1 — RX_VHFBUF_p
B1 — RX_VHFBUF_n
VR5 — A2 — VCC_LO
VR7 — A3 — GND_LO
C2 — VCC_PLL2
C1 — VSS_PLL2

VR7
CDMA_IF_P — J9 — VCC_ext
H10 — VSS_ext
CDMA_IF_N — C10 — RX_IF_p
AMPS_IF_N — B10 — RX_IF_n
AMPS_IF_P — A9 — RX_IF1_n
A10 — RX_IF1_p
D8 — VCC4
A8 — GND4
A5 — VCC8
B6 — GND8a
B5 — GND8b
VIO
R7103
8k2
C7105
10n
E10 — AGC_Ref
F9 — RX_IF_AGC
D9 — VCC5
D10 — GND5
RX_IF_AGC
R7104
27k
G2 — Rbias
G1 — Vref
VREFRF01
C7106
10n

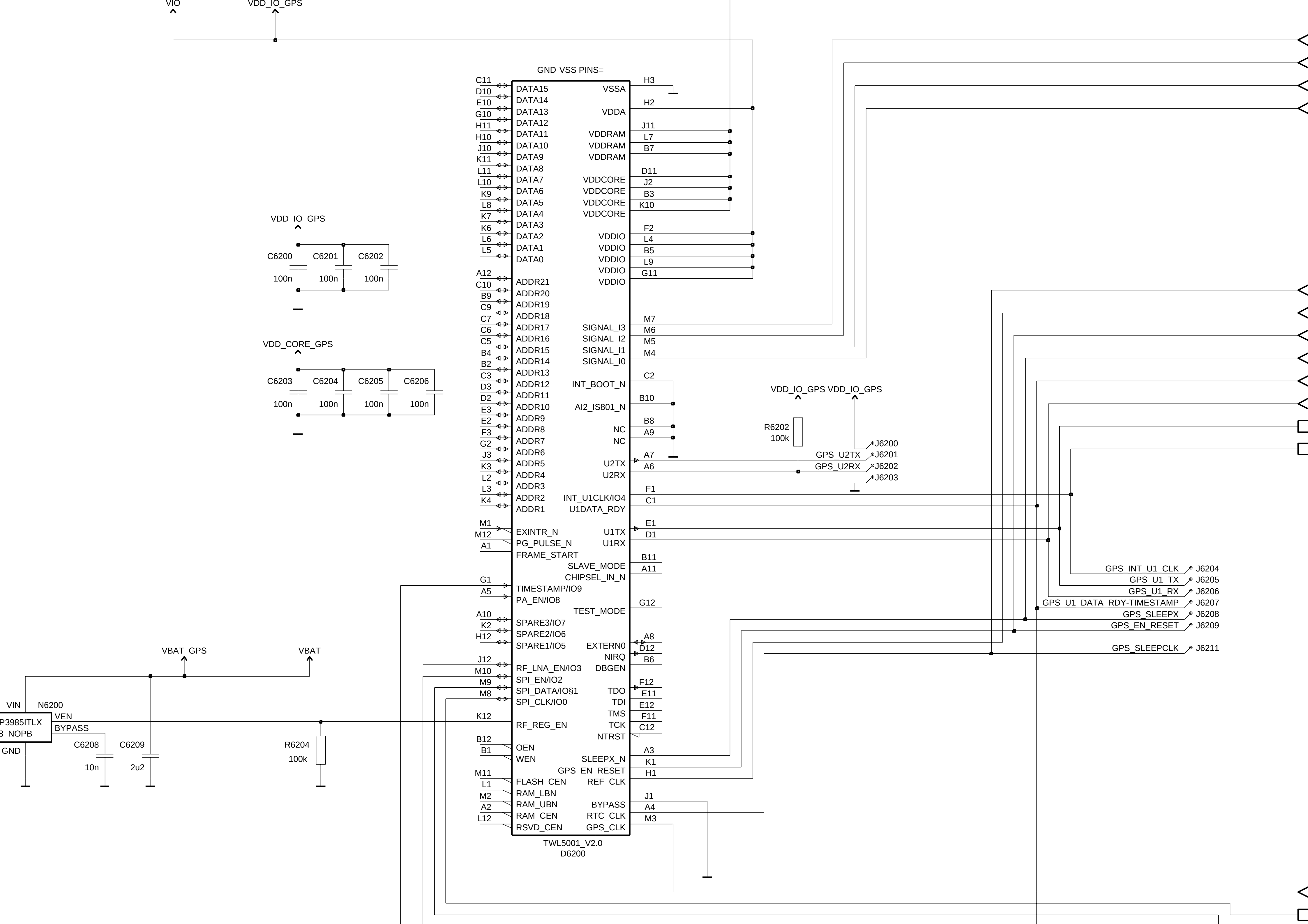
TEST_OUT — B2
GND_CH — D2
RFBusClk — K7 — RF_BUS_CLK
RFBusEn1X — H5 — RF_BUS_EN1X
VCO_ctrl — J10 — BAND_SEL_FE
LPB_SW — K10
VCC6 — C3 — VR5
GND6 — D1
VCC_DIGI — K9 — VIO
VSS_DIGI — K6
Reset — J5 — PURX
RFBusData — J6 — RF_BUS_DATA
OUT1 — H9 — BAND_SEL_VCO
OUT2 — H8
OUT3 — G8 — GAIN_CTL
OUT4 — F10
OUT5 — A7 — BATM01 — IF_SEL
OUT6 — A6
VCC_CTRL — B7 — VR7
GND_CTRL — B8
VCC_bias — F2 — VR5
GND_bias — H1
RXIP RXIN RXQP RXQN
RX_IP — J1 — RX_IP
RX_IN — H2 — RX_IN
RX_QP — K1 — RX_QP
RX_QN — K2 — RX_QN
IFI_TEST_p — E8
IFI_TEST_n — E9
IFQ_TEST_P — G9
IFQ_TEST_n — G10
GND_IF — A4
VCC_IF — B4 — VR5 VR5
VDD9 — J3
VSS9 — K3
OFFQ — K5
OFFI — K4



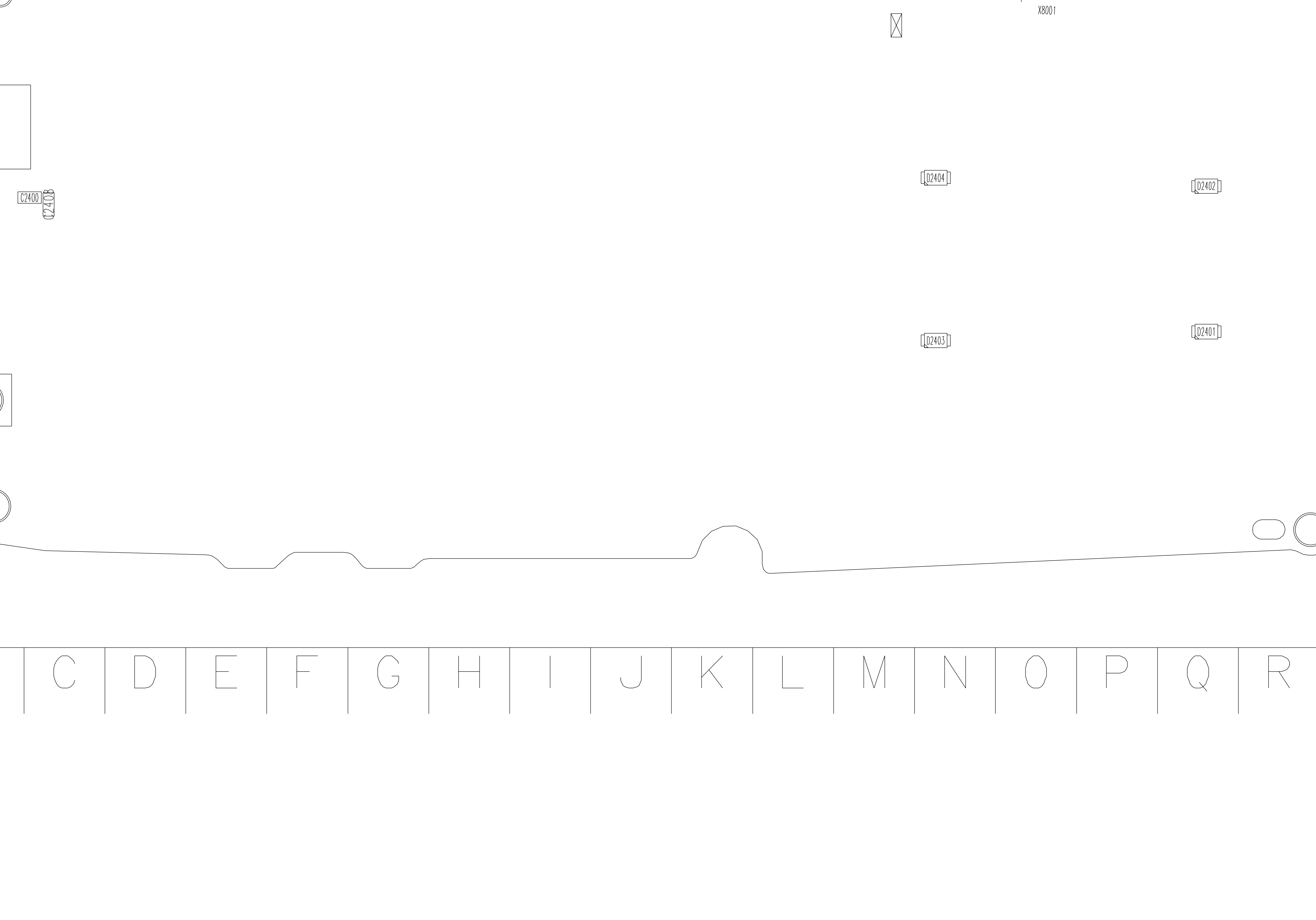
GPS_SPI_EN
GPS_SPI_DATA
GPS_SPI_CLK
GPS_B0
GPS_B1
GPS_B2
GPS_B3
GPS_CLK

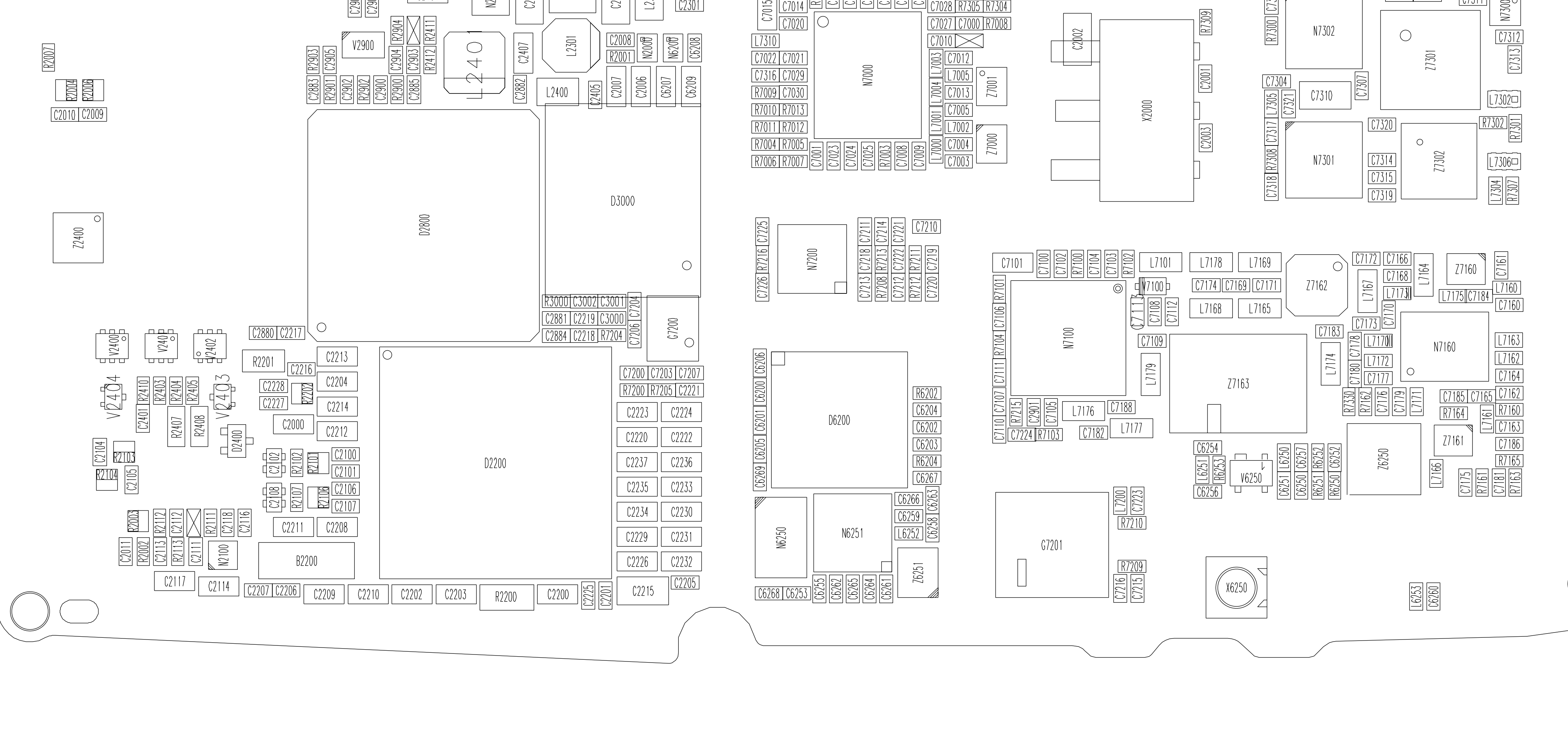
GPS_SPI_EN GPS_U1_RX GENIO(0)
GPS_SPI_DATA GPS_U1_TX GENIO(1)
GPS_SPI_CLK GPS_INT_U1_CLK GENIO(2)
GPS_U1_DATA_RDY-TIMESTAMP GENIO(4)
GPS_B0 GPS_EN_RESET GENIO(22)
GPS_B1
GPS_B2 GPS_SLEEPCLK PUSL(2)
GPS_B3 GPS_SLEEPX PUSL(1)
GPS_RFCLK RFCLK_GPS RFCLK_G
GPS_CLK

and 2128i only)

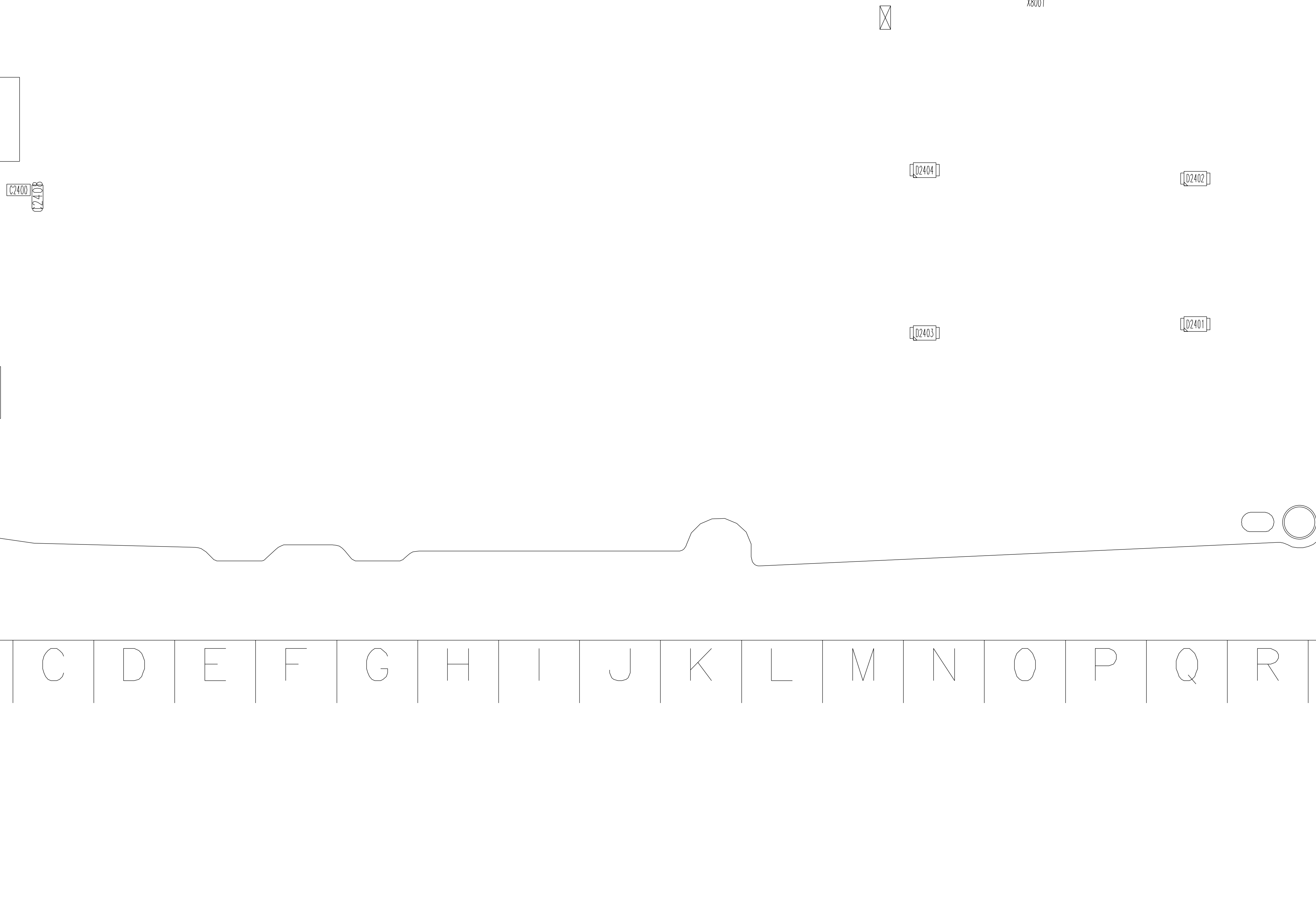


VIO
VDD_IO_GPS
GND VSS PINS=
TWL5001_V2.0
D6200
DATA15
DATA14
DATA13
DATA12
DATA11
DATA10
DATA9
DATA8
DATA7
DATA6
DATA5
DATA4
DATA3
DATA2
DATA1
DATA0
ADDR21
ADDR20
ADDR19
ADDR18
ADDR17
ADDR16
ADDR15
ADDR14
ADDR13
ADDR12
ADDR11
ADDR10
ADDR9
ADDR8
ADDR7
ADDR6
ADDR5
ADDR4
ADDR3
ADDR2
ADDR1
EXINTR_N
PG_PULSE_N
FRAME_START
TIMESTAMP/IO9
PA_EN/IO8
SPARE3/IO7
SPARE2/IO6
SPARE1/IO5
RF_LNA_EN/IO3
SPI_EN/IO2
SPI_DATA/IO§1
SPI_CLK/IO0
RF_REG_EN
OEN
WEN
FLASH_CEN
RAM_LBN
RAM_UBN
RAM_CEN
RSVD_CEN
VSSA
VDDA
VDDRAM
VDDRAM
VDDRAM
VDDCORE
VDDCORE
VDDCORE
VDDCORE
VDDIO
VDDIO
VDDIO
VDDIO
VDDIO
SIGNAL_I3
SIGNAL_I2
SIGNAL_I1
SIGNAL_I0
INT_BOOT_N
AI2_IS801_N
NC
NC
U2TX
U2RX
INT_U1CLK/IO4
U1DATA_RDY
U1TX
U1RX
SLAVE_MODE
CHIPSEL_IN_N
TEST_MODE
EXTERN0
NIRQ
DBGEN
TDO
TDI
TMS
TCK
NTRST
SLEEPX_N
GPS_EN_RESET
REF_CLK
BYPASS
RTC_CLK
GPS_CLK
C11 D10 E10 G10 H11 H10 J10 K11 L11 L10 K9 L8 K7 K6 L6 L5
A12 C10 B9 C9 C7 C6 C5 B4 B2 C3 D3 D2 E3 E2 F3 G2 J3 K3 L2 L3 K4
M1 M12 A1 G1 A5 A10 K2 H12 J12 M10 M9 M8 K12 B12 B1 M11 L1 M2 A2 L12
H3 H2 J11 L7 B7 D11 J2 B3 K10 F2 L4 B5 L9 G11 M7 M6 M5 M4 C2 B10 B8 A9 A7 A6 F1 C1 E1 D1 B11 A11 G12 A8 D12 B6 F12 E11 E12 F11 C12 A3 K1 H1 J1 A4 M3
VDD_IO_GPS
C6200
100n
C6201
100n
C6202
100n
VDD_CORE_GPS
C6203
100n
C6204
100n
C6205
100n
C6206
100n
VDD_IO_GPS VDD_IO_GPS
R6202
100k
J6200
GPS_U2TX
J6201
GPS_U2RX
J6202
J6203
GPS_INT_U1_CLK
J6204
GPS_U1_TX
J6205
GPS_U1_RX
J6206
GPS_U1_DATA_RDY-TIMESTAMP
J6207
GPS_SLEEPX
J6208
GPS_EN_RESET
J6209
GPS_SLEEPCLK
J6211
VBAT_GPS
VBAT
VIN
N6200
VEN
BYPASS
GND
P3985ITLX
8_NOPB
C6208
10n
C6209
2u2
R6204
100k

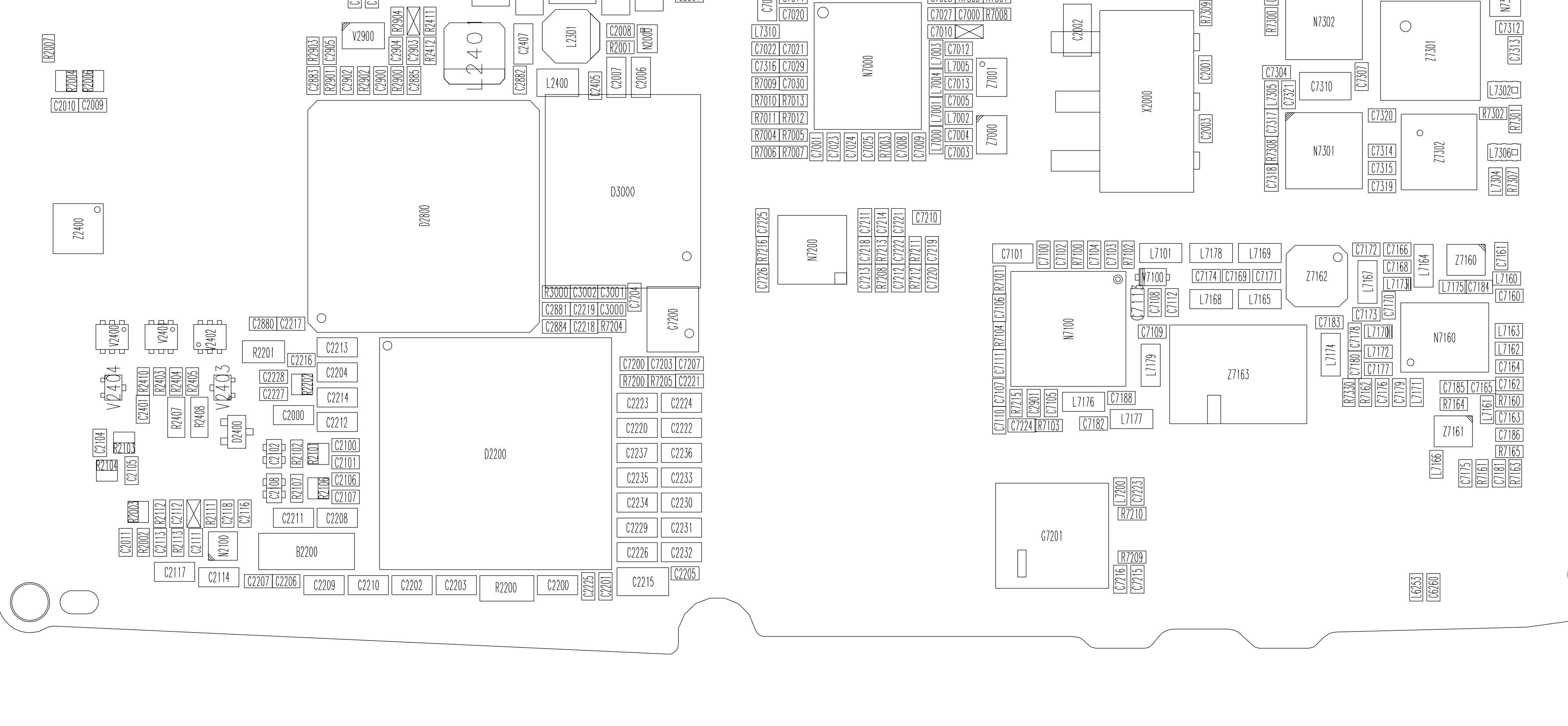

X8001
D2404
D2402
C2400
C2408
D2403
D2401
C
D
E
F
G
H
I
J
K
L
M
N
O
P
Q
R



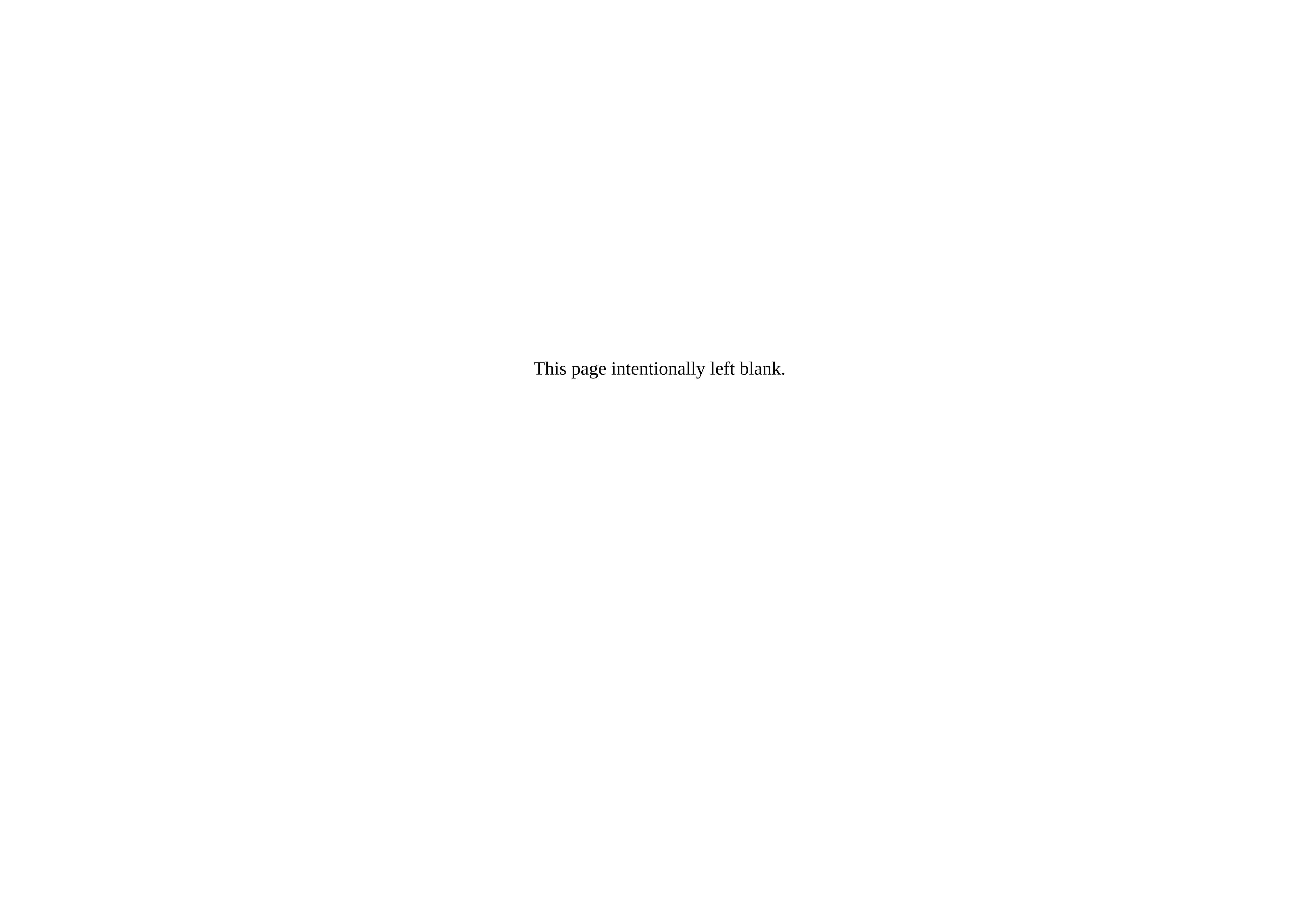
R	Q	P	O	N	M	L	K	J	I	H	G	F	E	D	C



X8001
C2400
C2408
D2404
D2402
D2403
D2401
C
D
E
F
G
H
I
J
K
L
M
N
O
P
Q
R



R	Q	P	O	N	M	L	K	J	I	H	G	F	E	D	C



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